

REMARKS

Favorable reconsideration of this application, in light of the preceding amendment and following remarks, is respectfully requested.

Claims 1-9, and 30 are pending in this application. None of the claims have been amended or cancelled, and no claims have been added. Claims 1 and 30 are the independent claims.

Applicants note with appreciation the Examiner's acknowledgement that certified copies of all priority documents have been received by the U.S.P.T.O. Office Action Summary at 12.

Applicants also respectfully note the present action indicates that the drawings have been accepted by the Examiner. Office Action Summary at 10.

Specification

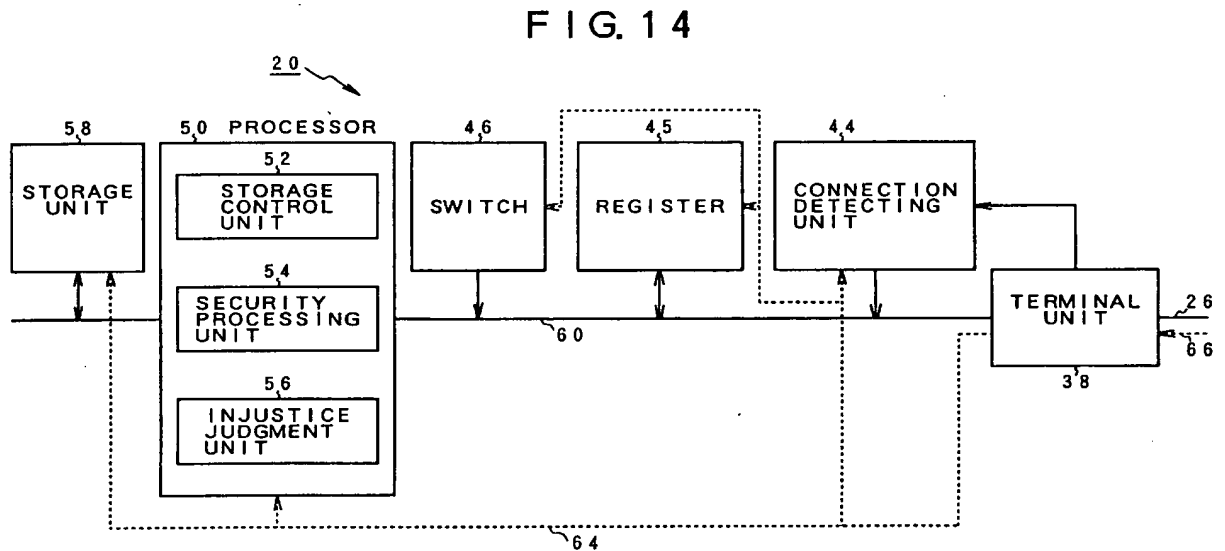
The specification has been amended to correct a typo error. More specifically, on page 18 in paragraph [0069], line 15, the word "signal" has been amended to the word "circuit."

Applicants hereby confirm their willingness to cooperate with the Examiner in the identification and correction of further minor errors within the specification. Applicants respectfully submit, however, that they are not presently aware of any such errors that would require correction.

Rejection under 35 U.S.C. § 102

Claims 1 and 30 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Isomura (U.S. Patent 6,374,310 hereinafter Isomura). Applicants respectfully traverse this rejection for the reasons detailed below.

Applicants submit that Isomura fails to disclose or suggest a dynamic random access memory (DRAM) device comprising “a DRAM core having memory cells for storing data, and a read protection unit for preventing the data stored in the memory cells before power is removed, from being read out when power is supplied” as recited in claim 1 and similarly recited in claim 30. Instead, Isomura discloses a memory board comprising “the processor and the storage unit as well as the connection detecting unit for detecting an unjust removal, the removal register and the switch, with the processor being provided with the functions of the storage control unit, the security processing unit and the injustice judgment unit.” Isomura, column 10, rows 12-23. The following is the figure from the Isomura reference illustrating this configuration.



Isomura discloses a security processing unit that is provided with a read inhibition unit in which “[t]he read inhibition unit is activated upon the reception of a post of judgment that an unjust removal has occurred from the injustice judgment unit in the power supply state after the

reconnection of the memory board, and inhibits the execution of a read control of the storage unit in response to the read demand to the storage control unit from the exterior.” Isomura, column 10, rows 51-58. In other words, Isomura discloses a *system level* apparatus for storing information and prohibiting an unjust read out of the information after reconnection to the power supply rather than a *semiconductor* memory device comprising “a DRAM core having memory cells for storing data, and a read protection unit for preventing the data stored ... from being read out when power is supplied” as recited in claim 1 and similarly recited in claim 30.

Because Isomura fails to disclose each and every feature of the claimed invention, Isomura cannot anticipate or render the claimed invention as recited in claims 1 and 30 obvious to one skilled in the art. Claims 2-9 are also allowable by virtue of their dependency on independent claim 1, and for the features recited therein.

Accordingly, Applicants respectfully request reconsideration and withdrawal of the rejections to independent claims 1 and 30 and all claims dependent upon them.

Allowable Subject Matter

Applicants appreciate that claims 2-9 would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. However, it is submitted that claims 2-9 are presently allowable in view of the previous remarks.

CONCLUSION

In view of the above remarks and amendments, the Applicants respectfully submit that each of the pending objections and rejections has been addressed and overcome, placing the present application in condition for allowance. A notice to that effect is respectfully requested. If the Examiner believes that personal communication will expedite prosecution of this application, the Examiner is invited to contact the undersigned.

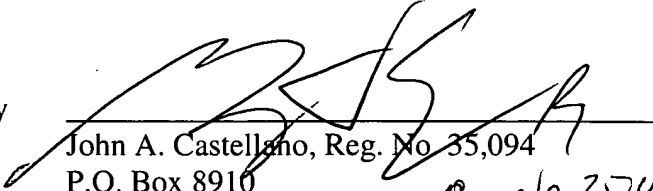
Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact John A. Castellano, at the telephone number of the undersigned below.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNESS, DICKY, & PIERCE, P.L.C.

By



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